



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7233

SERIAL NUMBER 10/687,096	FILING OR 371(c) DATE 10/15/2003 RULE	CLASS 438	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 108298737US
-----------------------------	--	--------------	------------------------	---------------------------------------

## APPLICANTS

James L. Voelz, Meridian, ID;

\*\* CONTINUING DATA \*\*\*\*\* SWS

\*\* FOREIGN APPLICATIONS \*\*\*\*\* SWC

IF REQUIRED, FOREIGN FILING LICENSE GRANTED \*\*

01/20/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 9	TOTAL CLAIMS 67	INDEPENDENT CLAIMS 10
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials <i>SWC</i>				

## ADDRESS

25096

## TITLE

Methods for wafer-level packaging of microelectronic devices and microelectronic devices formed by such methods

FILING FEE RECEIVED 2218	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
--------------------------------	---	---